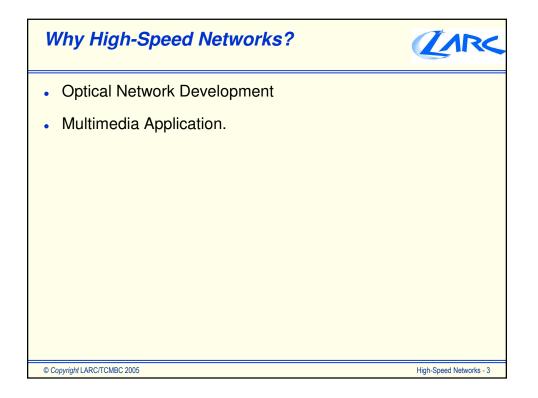
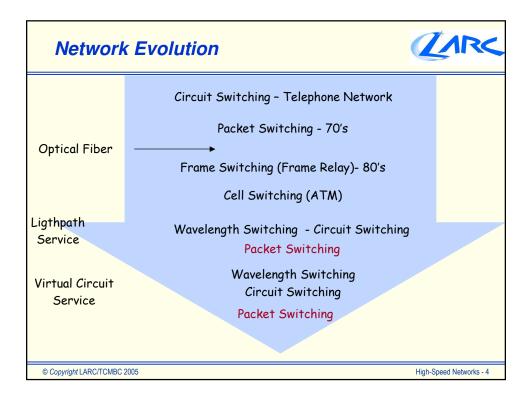
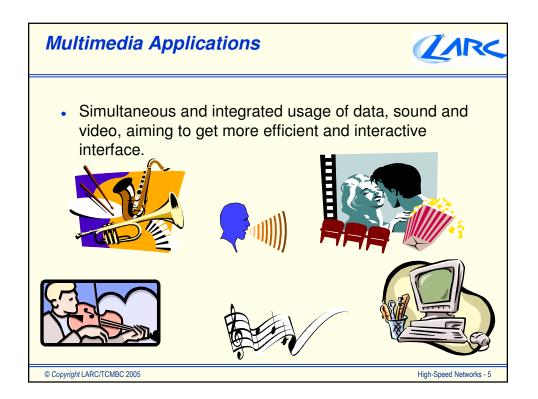
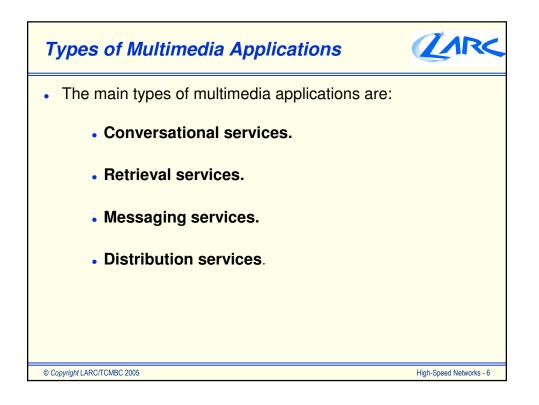


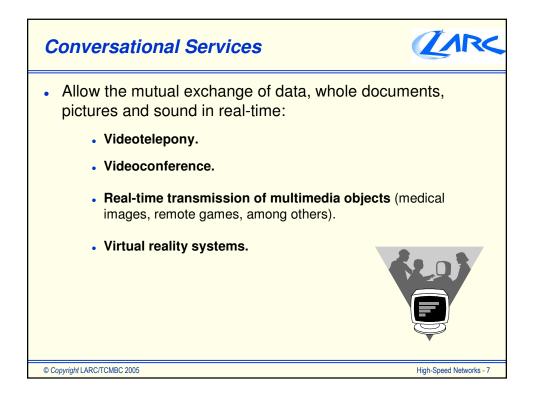
Agenda	ZARC.
Why High-Speed Networks?	
Switching Techniques	
Circuit Switching.	
Packet Switching.	
Cell Switching	
 High-Speed Network Technologies. 	
• ATM	
• MPLS	
IP Networks.	
Final Considerations	
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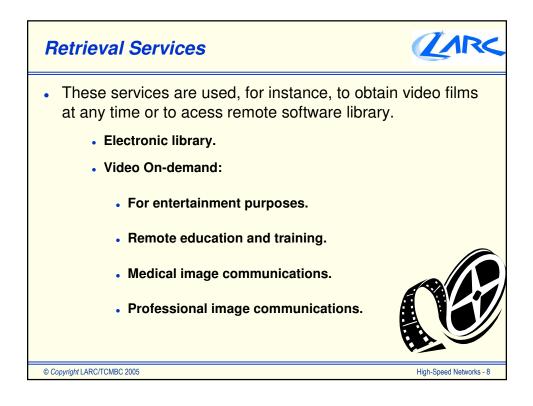


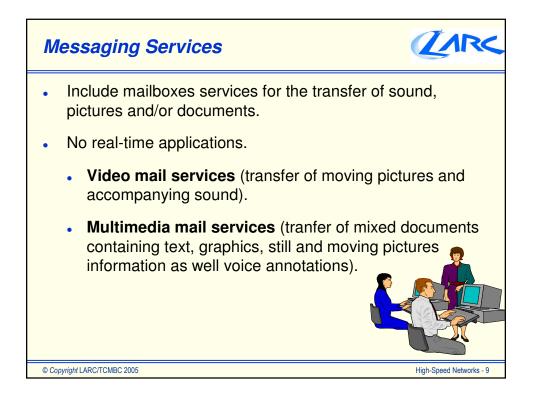




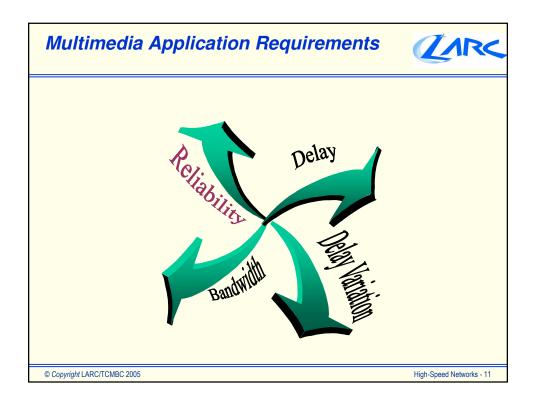


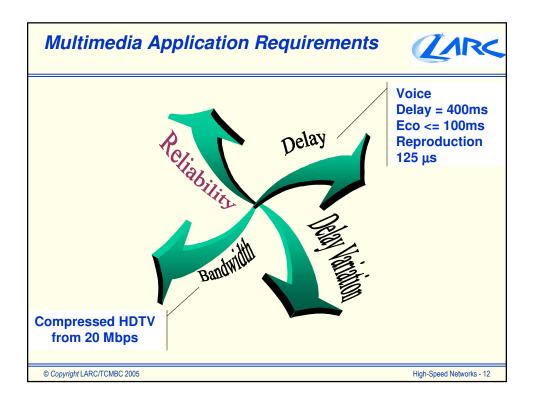




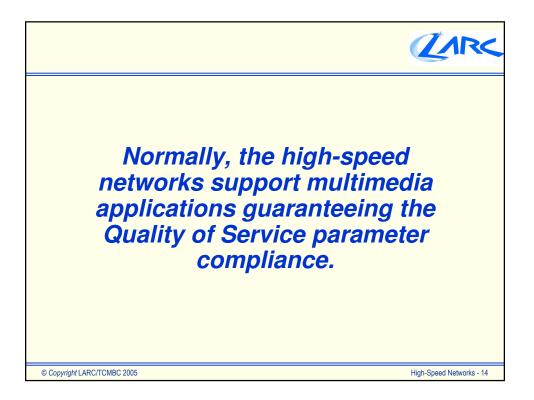




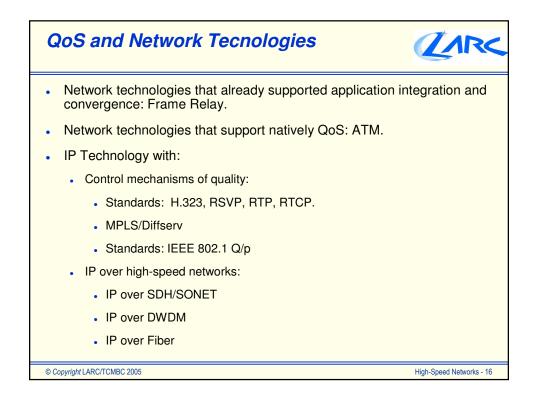


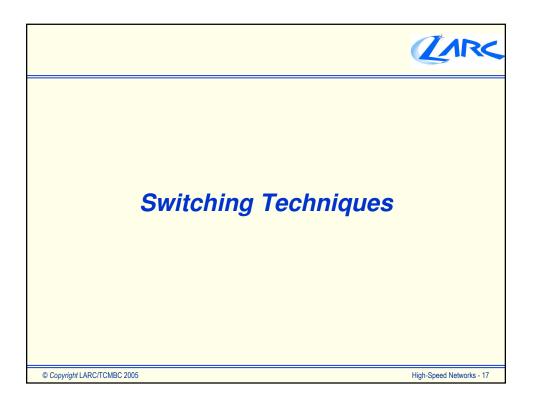


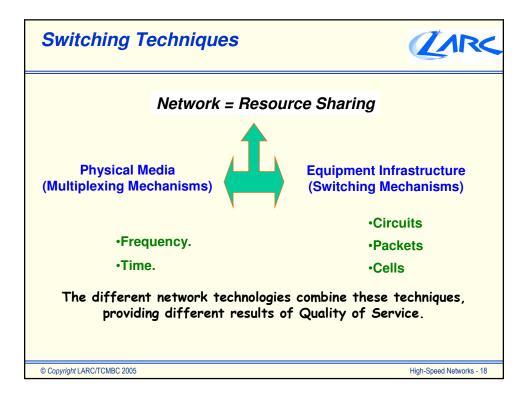
ferent mult rameters.	imedia a	applications	s work pr	operly on d	ifferent (QoS
Application Requireme nts	Data	Graphic Data	Voice	Interactive Voice	Video	Real-Time Video
Bandwidth	L	Н	н	Н	VH	VH
Delay	L	L	н	VH	Н	VH
Delay Variation	L	L	н	VH	Н	VH
Error Rate	н	VH	L	L	L/H	VH/

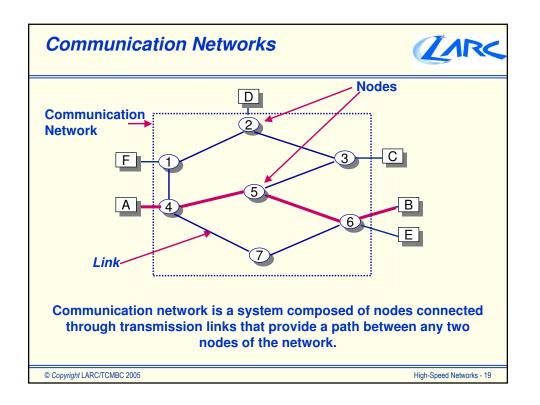


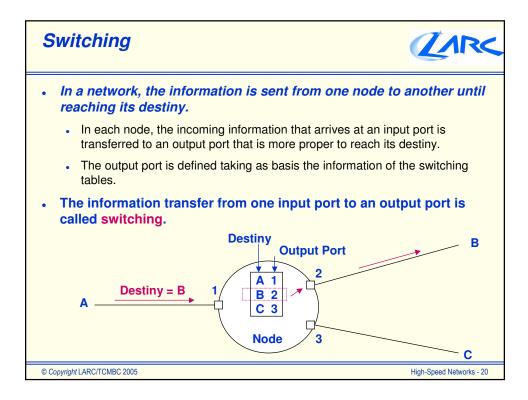
Quality of Service in Hig	h-Speed Network		
• Delay	 Data prioritization according to the channel type. rt-VBR > nrt-VBR 		
Delay Variation	 Synchronization Mechanisms Ex: time stamp. 		
Bandwidth	 Bandwidth reservation via signaling. 		
Reliability	Error detection and recovery		
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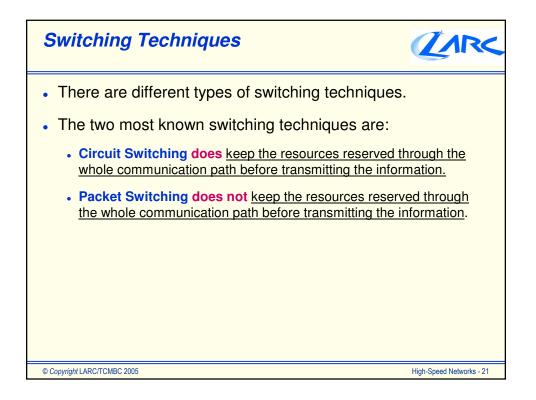


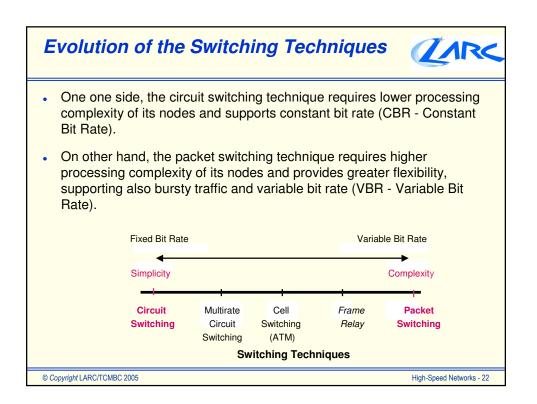


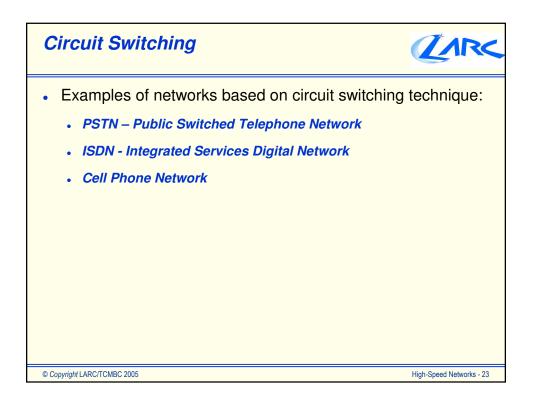


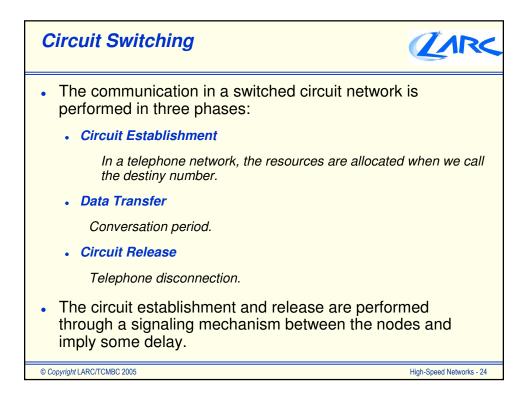


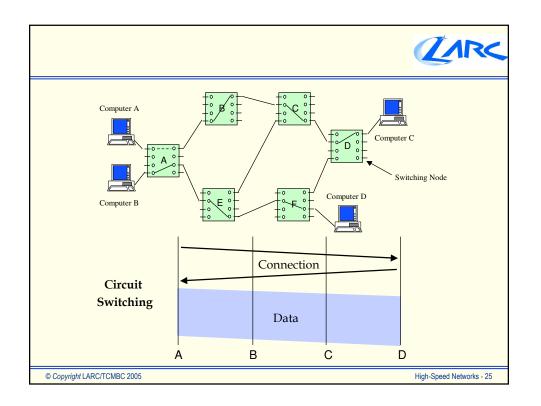


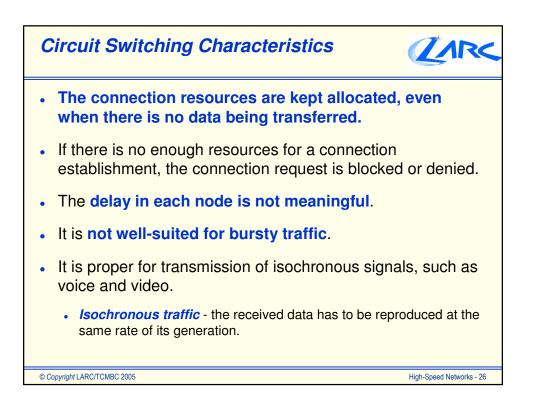


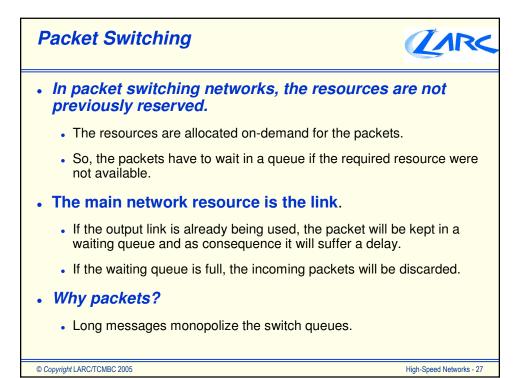


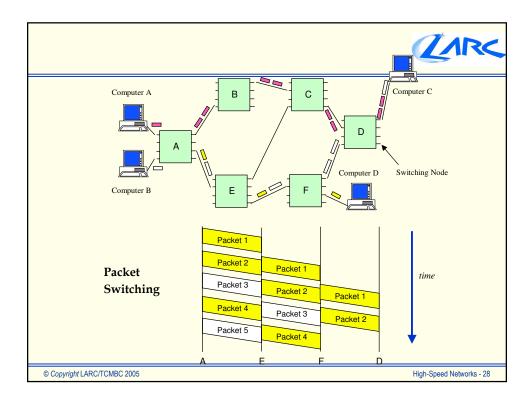


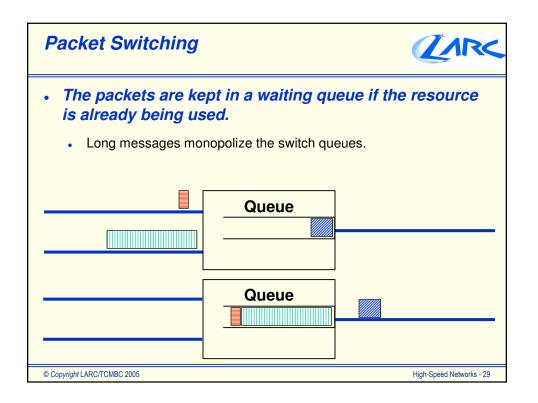


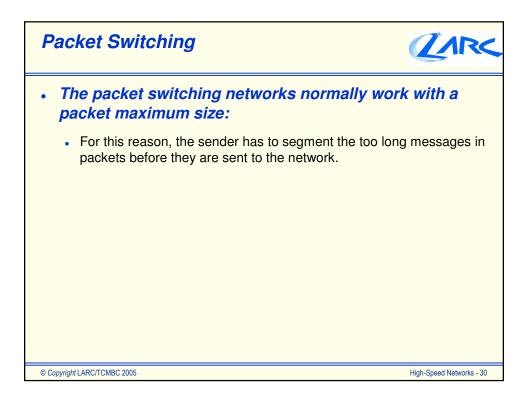


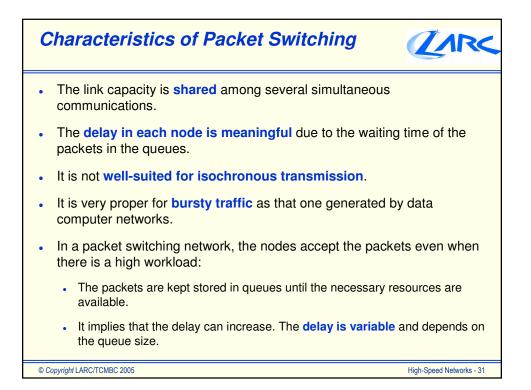


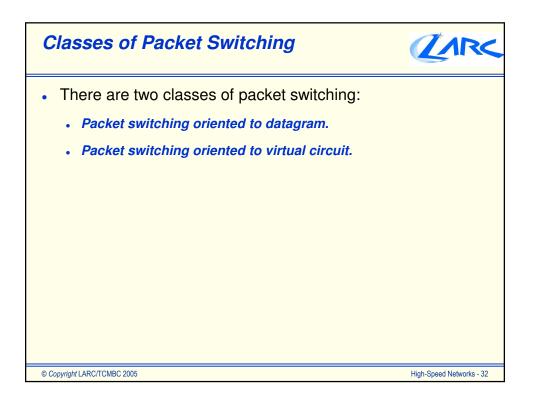


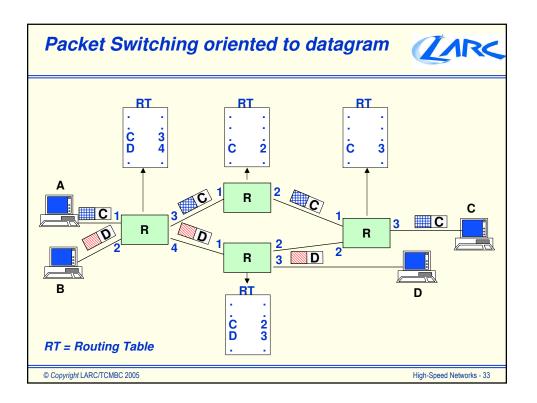


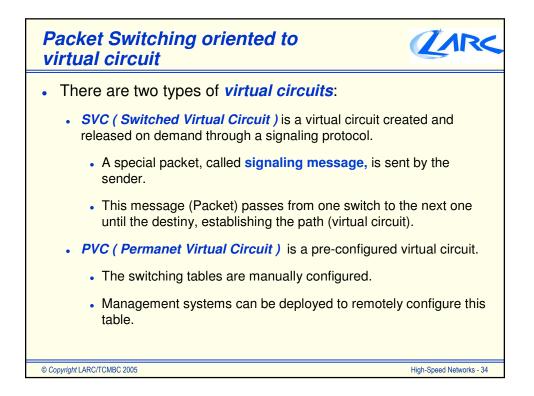


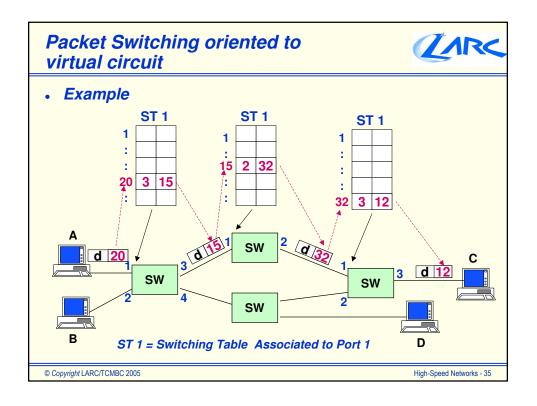




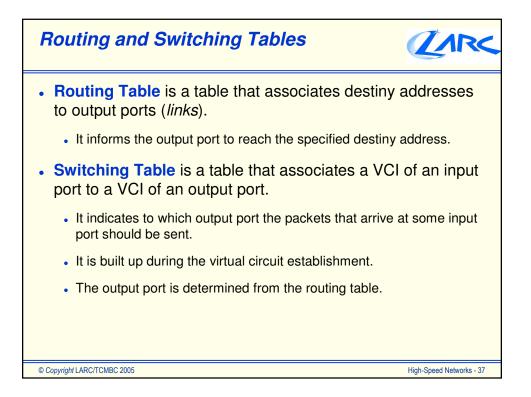


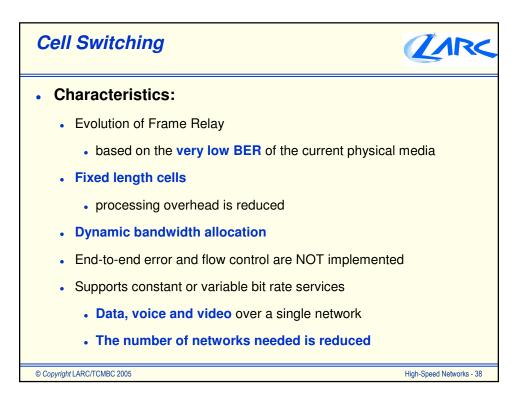


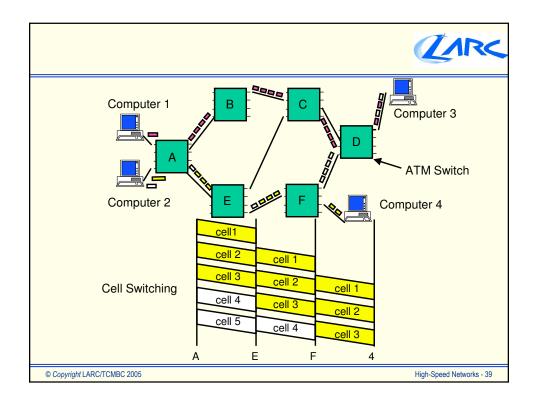


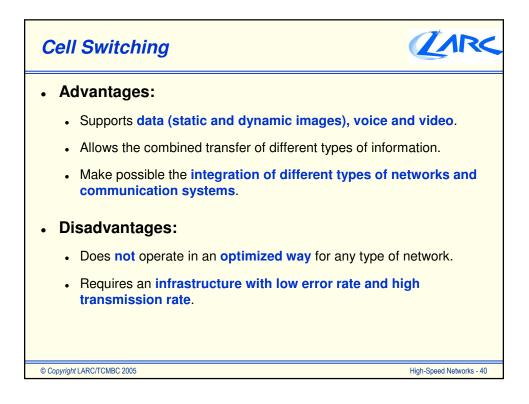


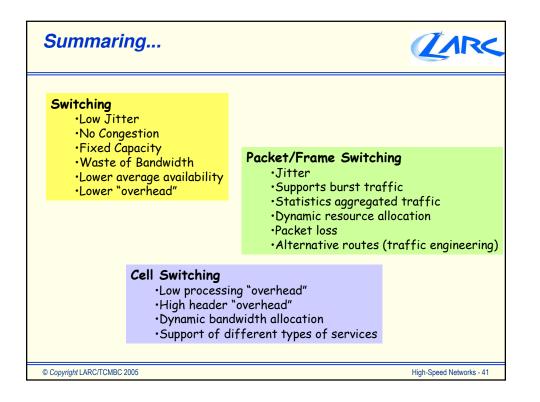
Datagram x virtual circuit	
Datagram	Virtual Circuit
All packets carry the complete address.	The packets carry an identifier
Slow Switching: The routing is performed on demand.	Fast Switching: The routing is pre-performed.
The datagrams can be discarded during congestion periods.	It is possible to avoid congestion during the virtual circuit establishment.
Examples:	Examples:
IP, IPX, Ethernet, Token Ring	X.25, Frame Relay, ATM, MPLS
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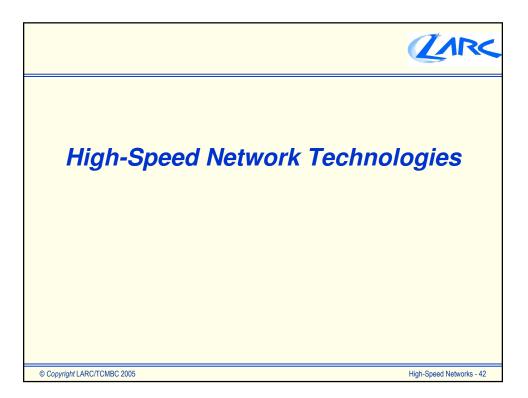


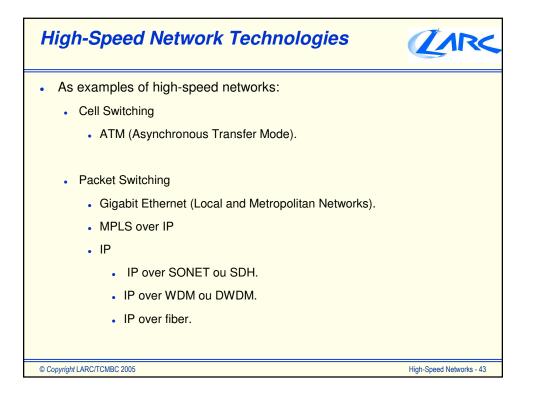




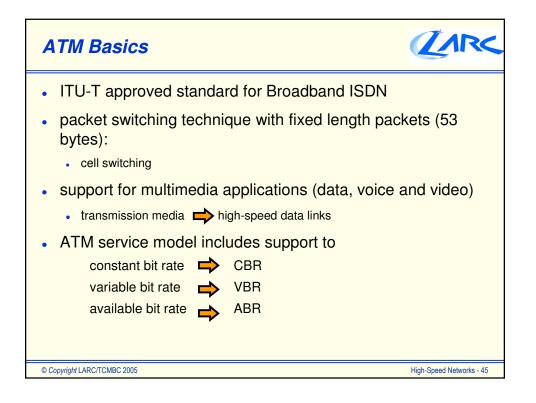


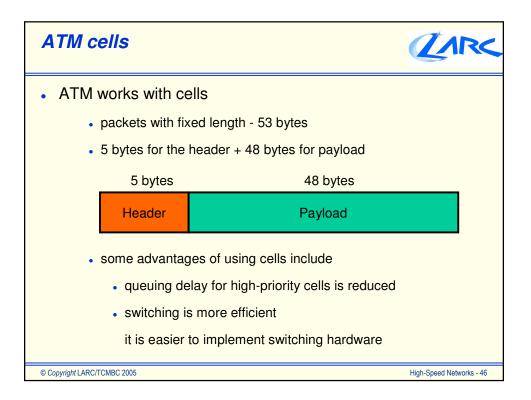


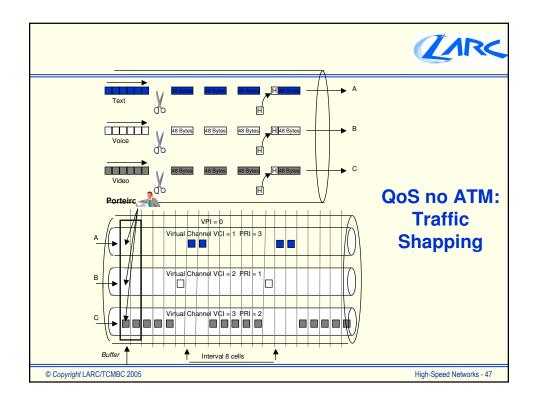


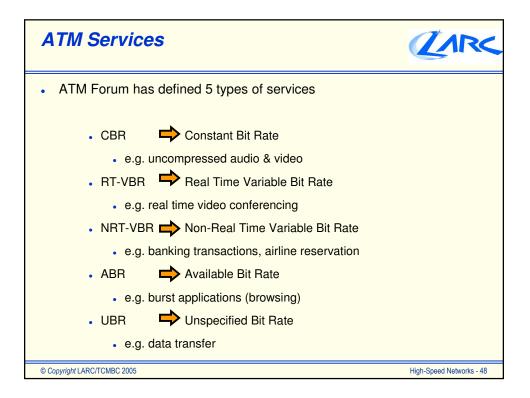


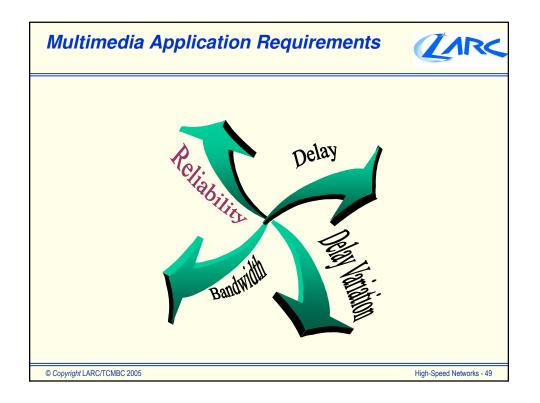


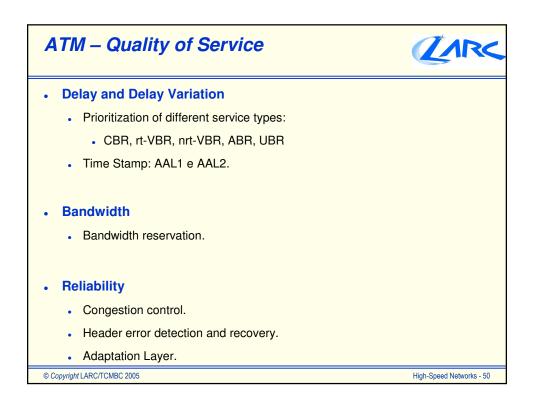


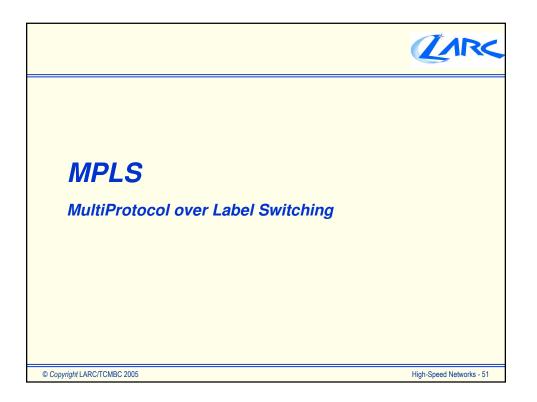


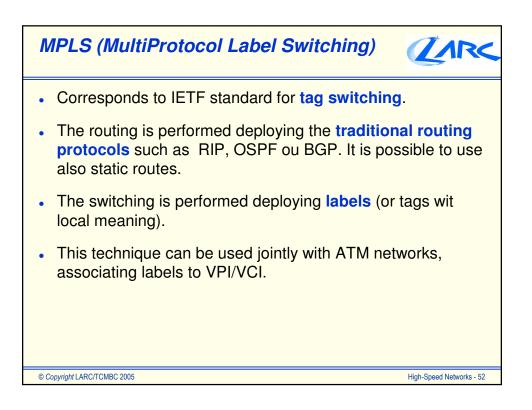


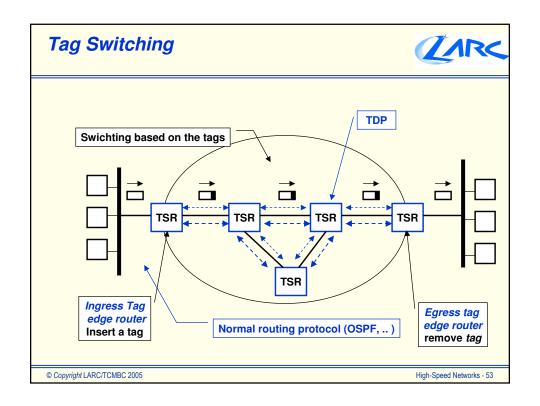












Tag Switching				ZARC		
• [Example	of tran	smission in a	a network w	ith tag switching	
					0 0	
	11	nput tag	output tag	next step	output interface	1
	at TSR A	100	6	TSR B	if1	
	at TSR B	6	6	TSR E	if1	
	at TSR C	17	5	TSR D	if2	
	at TSR D	5	6 ?	TSR E	if0	
	at TSR E	6	?	TSR E	ifO	1

